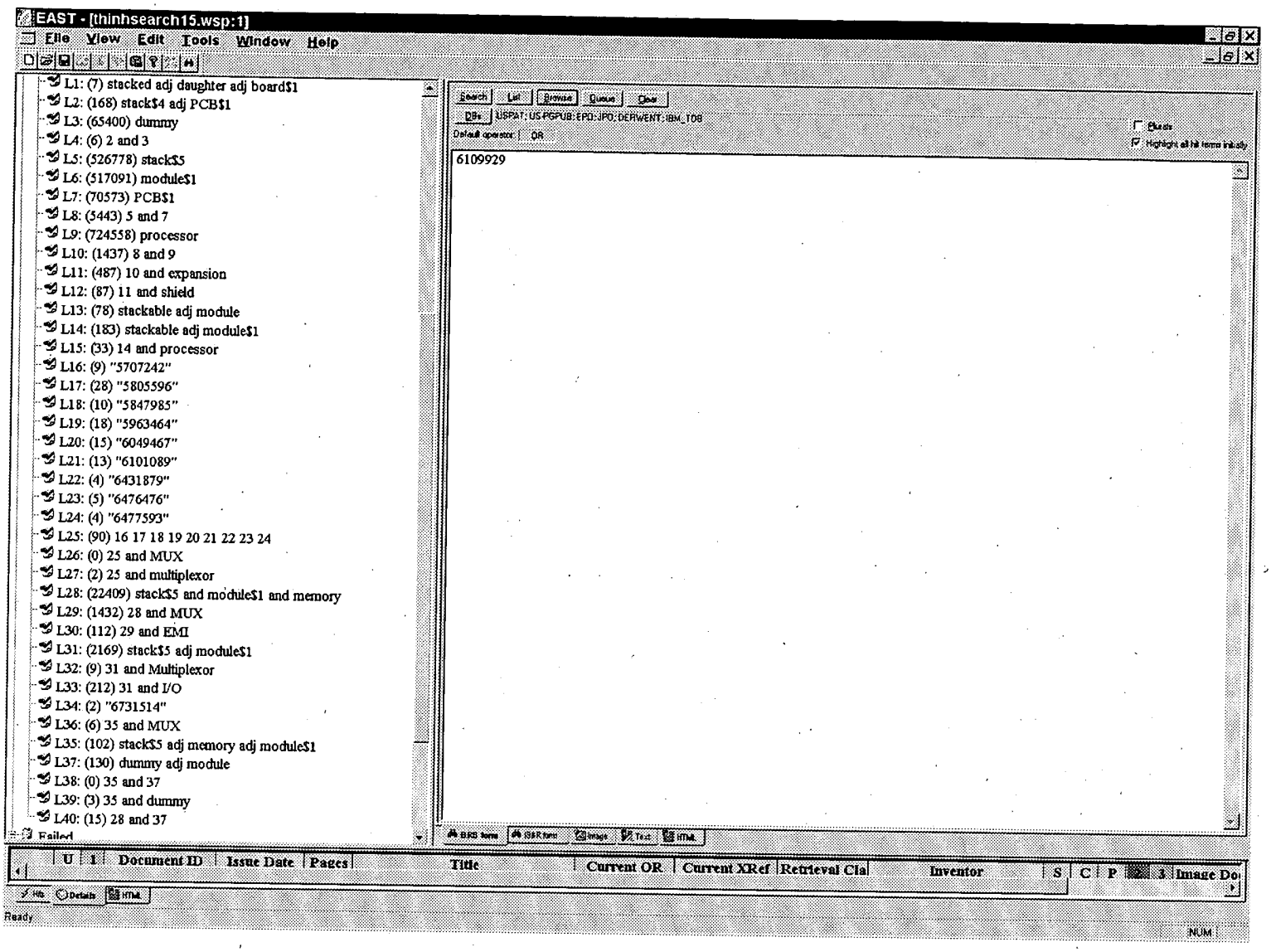
L Number	Hits	Search Text	DB	Time stamp
1	7	stacked adj daughter adj board$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:31
2	168	stack$4 adj PCB$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:32
3	65400	dummy	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:32
4	6	(stack$4 adj PCB$1) and dummy	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:33
5	526778	stack$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:33
6	517091	module$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:34
7	70573	PCB$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:34
8	5443	stack$5 and PCB$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:36
9	724558	processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:36
10	1437	(stack$5 and PCB$1) and processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:38
11	487	((stack$5 and PCB$1) and processor) and expansion	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:39
12	87	(((stack$5 and PCB$1) and processor) and expansion) and shield	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:49

13	78	stackable adj module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:49
14	183	stackable adj module$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 09:50
15	33	(stackable adj module$1) and processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:07
16	9	"5707242"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:07
17	28	"5805596"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:08
18	10	"5847985"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:08
19	18	"5963464"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:08
20	15	"6049467"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:08
21	13	"6101089"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:09
22	4	"6431879"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:09
23	5	"6476476"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:09
24	4	"6477593"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:10

25	90	"5707242" "5805596" "5847985" "5963464" "6049467" "6101089" "6431879" "6476476" "6477593"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:10
26	0	("5707242" "5805596" "5847985" "5963464" "6049467" "6101089" "6431879" "6476476" "6477593") and MUX	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:10
27	2	("5707242" "5805596" "5847985" "5963464" "6049467" "6101089" "6431879" "6476476" "6477593") and multiplexor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:19
28	22409	stack$5 and module$1 and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:20
29	1432	(stack$5 and module$1 and memory) and MUX	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:20
30	112	((stack$5 and module$1 and memory) and MUX) and EMI	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:24
31	2169	stack$5 adj module$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:25
32	9	(stack$5 adj module$1) and Multiplexor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:58
33	212	(stack$5 adj module$1) and I/O	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 11:28
34	2	"6731514"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 12:44
36	6	(stack$5 adj memory adj module$1) and MUX	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 12:45
35	102	stack$5 adj memory adj module$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 13:22

37	130	dummy adj module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 13:23
38	0	(stack$5 adj memory adj module$1) and (dummy adj module)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 13:23
39	3	(stack$5 adj memory adj module$1) and dummy	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 13:25
40	15	(stack$5 and module$1 and memory) and (dummy adj module)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/19 13:52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File View Edit Tools Window Help
L1: (7) stacked adj daughter adj board$1
L2: (168) stack$4 adj PCB$1
L3: (65400) dummy
L4: (6) 2 and 3
L5: (526778) stack$5
L6: (517091) module$1
L7: (70573) PCB$1
L8: (5443) 5 and 7
L9: (724558) processor
L10: (1437) 8 and 9
L11: (487) 10 and expansion
L12: (87) 11 and shield
L13: (78) stackable adj module
L14: (183) stackable adj module$1
L15: (33) 14 and processor
L16: (9) "5707242"
L17: (28) "5805596"
L18: (10) "5847985"
L19: (18) "5963464"
L20: (15) "6049467"
L21: (13) "6101089"
L22: (4) "6431879"
L23: (5) "6476476"
L24: (4) "6477593"
L25: (90) 16 17 18 19 20 21 22 23 24
L26: (0) 25 and MUX
L27: (2) 25 and multiplexor
L28: (22409) stack$5 and module$1 and memory
L29: (1432) 28 and MUX
L30: (112) 29 and EMI
L31: (2169) stack$5 adj module$1
L32: (9) 31 and Multiplexor
L33: (212) 31 and I/O
L34: (2) "6731514"
L36: (6) 35 and MUX
L35: (102) stack$5 adj memory adj module$1
L37: (130) dummy adj module
L38: (0) 35 and 37
L39: (3) 35 and dummy
L40: (15) 28 and 37
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DBs USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
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U 1 Document ID Issue Date Pages Title Current OR Current XRef Retrieval Cla Inventor S C P $ Image Do
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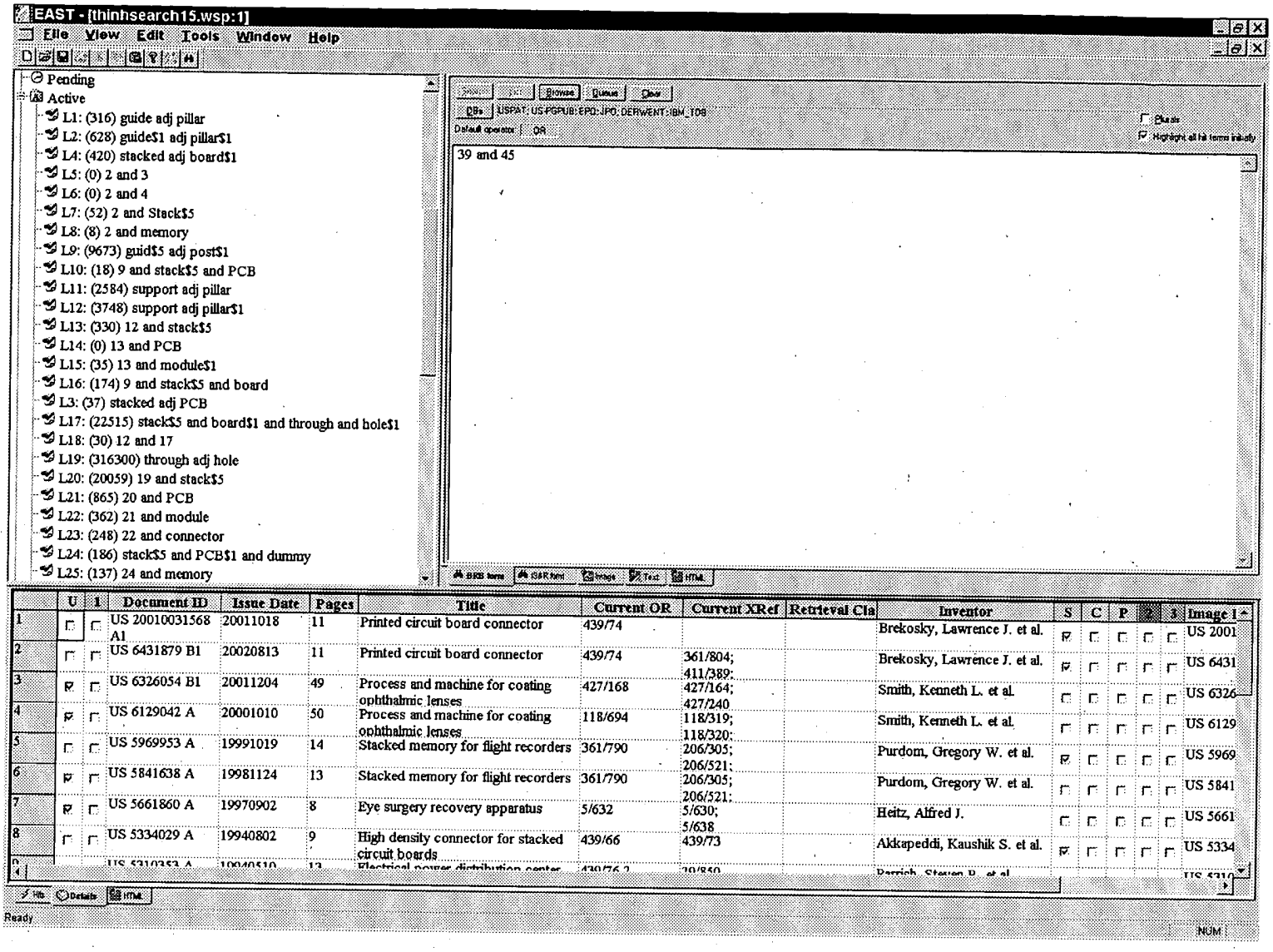

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	2	3	Image 1
1	☐	☐	US 20010031568 A1	20011018	11	Printed circuit board connector	439/74			Brekosky, Lawrence J. et al.	☑	☐	☐	☐	☐	US 2001
2	☐	☐	US 6431879 B1	20020813	11	Printed circuit board connector	439/74	361/804; 411/389;		Brekosky, Lawrence J. et al.	☑	☐	☐	☐	☐	US 6431
3	☑	☐	US 6326054 B1	20011204	49	Process and machine for coating ophthalmic lenses	427/168	427/164; 427/240		Smith, Kenneth L. et al.	☐	☐	☐	☐	☐	US 6326
4	☑	☐	US 6129042 A	20001010	50	Process and machine for coating ophthalmic lenses	118/694	118/319; 118/320;		Smith, Kenneth L. et al.	☐	☐	☐	☐	☐	US 6129
5	☐	☐	US 5969953 A	19991019	14	Stacked memory for flight recorders	361/790	206/305; 206/521;		Purdom, Gregory W. et al.	☑	☐	☐	☐	☐	US 5969
6	☑	☐	US 5841638 A	19981124	13	Stacked memory for flight recorders	361/790	206/305; 206/521;		Purdom, Gregory W. et al.	☐	☐	☐	☐	☐	US 5841
7	☑	☐	US 5661860 A	19970902	8	Eye surgery recovery apparatus	5/632	5/630; 5/638		Heitz, Alfred J.	☐	☐	☐	☐	☐	US 5661
8	☐	☐	US 5334029 A	19940802	9	High density connector for stacked circuit boards	439/66	439/73		Akkapeddi, Kaushik S. et al.	☑	☐	☐	☐	☐	US 5334
9	☐	☐	US 5310353 A	19940510	13	Electrical power distribution center	439/76.2	29/850		Parrish, Steven R. et al.	☐	☐	☐	☐	☐	US 5310

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- L21: (865) 20 and PCB
- L22: (362) 21 and module
- L23: (248) 22 and connector
- L24: (186) stack$5 and PCB$1 and dummy
- L25: (137) 24 and memory
- L26: (952) External adj memory adj interface
- L27: (169) 26 and stack$5 and module$1
- L28: (3110) stack$5 adj memory
- L29: (6) 27 and 28
- L30: (0) 4 and 26
- L32: (2) 26 and 31
- L31: (183) stackable adj module$1
- L33: (12) Stacked adj printed adj boards
- L34: (103005) 361/$.ccls.
- L35: (2350) 34 and stack$5 and hole
- L36: (483) 35 and guide
- L37: (236) 35 and I/O
- L38: (1280) via adj post
- L39: (420) stacked adj board$1
- L40: (1) 38 and 39
- L41: (9) "5707242"
- L42: (241) mainboard
- L43: (14346) motherboard
- L44: (19) 39 and 43
- L45: (136015) spacers
- L46: (26) 39 and 45

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DBs USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

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	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	?	I	Image I
1	☐	☐	US 20010031568 A1	20011018	11	Printed circuit board connector	439/74			Brekosky, Lawrence J. et al.	☑	☐	☐	☐	☐	US 2001
2	☐	☐	US 6431879 B1	20020813	11	Printed circuit board connector	439/74	361/804; 411/389;		Brekosky, Lawrence J. et al.	☑	☐	☐	☐	☐	US 6431
3	☑	☐	US 6326054 B1	20011204	49	Process and machine for coating ophthalmic lenses	427/168	427/164; 427/240		Smith, Kenneth L. et al.	☐	☐	☐	☐	☐	US 6326
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8	☐	☐	US 5334029 A	19940802	9	High density connector for stacked circuit boards	439/66	439/73		Akkapeddi, Kaushik S. et al.	☑	☐	☐	☐	☐	US 5334
9			US 5310353 A	19940510	13	Electrical power distribution center	439/76.2	29/850		Parrish, Steven R. et al.						US 5310

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